

*"ST recognizes the semiconductor industry needs to reduce the use of PFAS. In close coordination with industry players, we are working to identify and implement non-PFAS technical solutions, looking at the short-, mid- and long-term. This is in line with our longstanding commitment to minimize our environmental footprint at every stage of our products' lifecycle."*

#### **Additional public information – NON ST**

##### **1. PFAS Consortium**

All documents there are publicly available – the whole set of documents gives the most accurate description of PFAS uses in semiconductor sector

<https://www.semiconductors.org/pfas/>

##### **2. PFAS EU ECHA Page**

<https://echa.europa.eu/hot-topics/perfluoroalkyl-chemicals-pfas>

The update on the regulatory process are reported @

[https://echa.europa.eu/documents/10162/67348133/upfas\\_evaluation\\_state\\_of\\_play\\_en.pdf/d1ad6892-e726-84a7-d2dd-74bbf8fa09af?t=1727262011016](https://echa.europa.eu/documents/10162/67348133/upfas_evaluation_state_of_play_en.pdf/d1ad6892-e726-84a7-d2dd-74bbf8fa09af?t=1727262011016)

Here it can be seen that the semiconductor sector is not yet mentioned

As a conclusion there is no news on how the restriction will evolve